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INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

Docket Number (Optional)	Application Number
11675.99.1	09/300,363

Applicant(s): Philip J. Ireland, et al. #8

Filing Date	Group Art Unit
April 26, 1999	2815

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
MEW	1.	5,362,666	11/94	Dennison	437	52	
MEW	2.	5,652,164	7/97	Dennison, et al.	437	52	

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation YES	Translation NO

OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

MEW	3.	H. Itoh, et al., "Two Step Deposited Rugged Surface (TDRS) Storagenode and Self Aligned Bitline-Contact Penetrating Cellplate (SABPEC) for 64MbDRAM STC Cell, Oki Electric Industry Co., Ltd., VLSI R&D Center (Japan), pp. 9-10.

EXAMINER: Matthew E. Warren

DATE CONSIDERED: 9/28/01

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.